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FORM PTO-1449 (SUBSTITUTE)		Attorney Docket No.: MAS-FIN-410 Appl. No.: 10 698,081	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Applicant: RUDOLF LEHNER	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Filing Date: October 30, 2003 Group Art Unit: 2815	

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
jjbc	A	6,441,500 B1	8/27/02	Sumikawa et al.			
	B	2003/0085474 A1	5/8/03	Frankowsky et al.			
	C	6,205,745 B1	3/27/01	Dudderar et al.			
	D	5,966,903	10/19/99	Dudderar et al.			
jjbc	E	5,804,882	9/8/98	Tsukagoshi et al.			
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
jjbc	J	101 45 468 C1	1/16/03	Germany			
	K	198 22 512 A1	10/21/99	Germany			
	L	0 962 960 A2	12/8/99	Europe			
	M	1 032 030 A2	8/30/00	Europe			
jjbc	N	96/37913	11/28/96	WIPO			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER	DATE CONSIDERED	
Jasmine Clark	10/07/04	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
me	J	57031148 A	2/19/82	Japan	1	1	
me	K	2000269239 A	9/29/00	Japan	1	1	
me	L	03046242 A	2/27/91	Japan	1	1	
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
me		Theuß, H. et al.: "Mounting Process for Semiconductor Dice", Siemens Technik Report, 2001, pp. 1-2					
me							
EXAMINER jasmine Clark				DATE CONSIDERED 10/07/04			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							